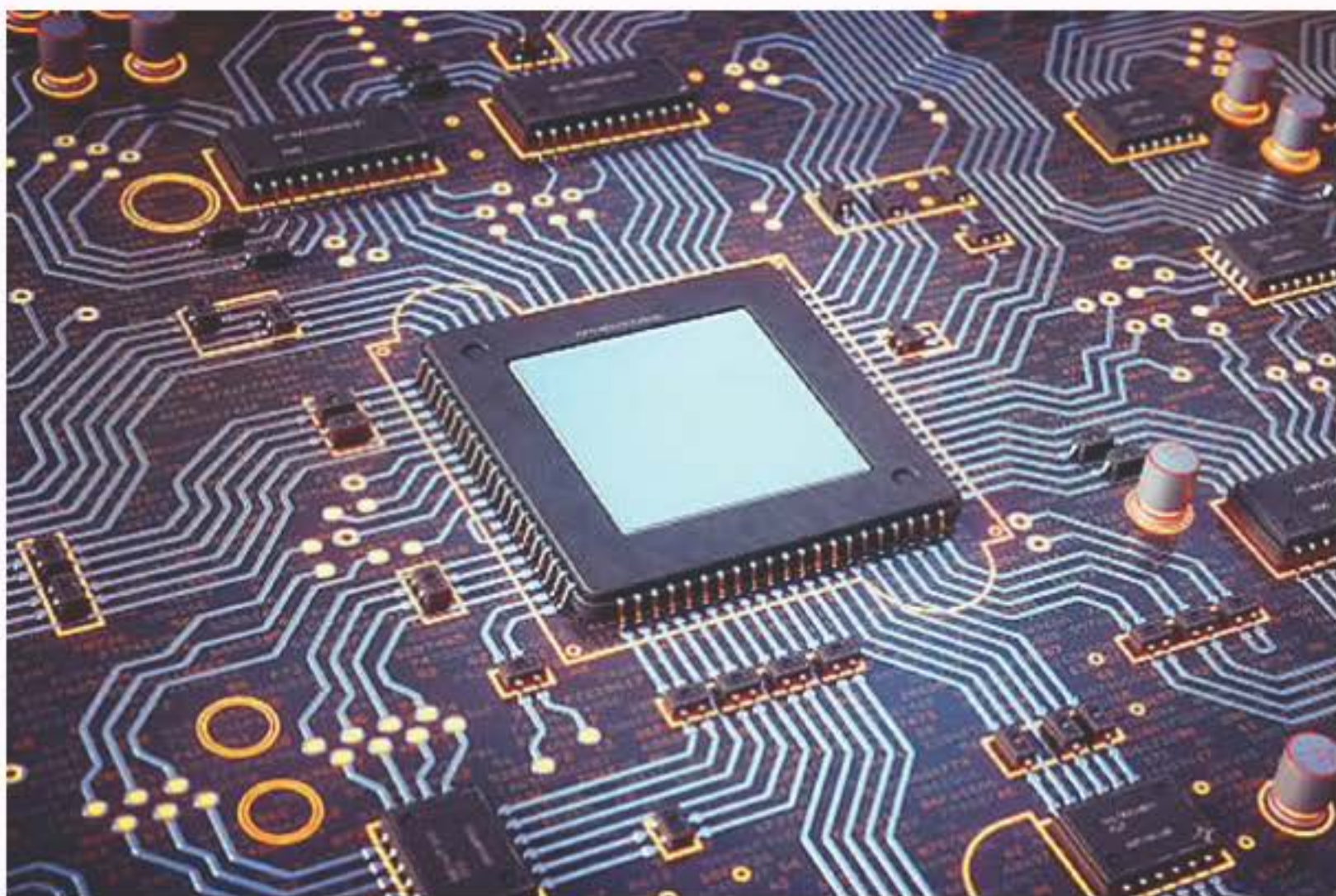


Here's How India's Display Fab Applicants Vedanta And ELEST Are Planning To Get The Technology Know-How



Semiconductor manufacturing. (Representative image)

Snapshot

- *The government's \$10 billion incentive policy aims at attracting display fabs and boost semiconductor ecosystem in the country.*

As part of its push to increase self-sufficiency and reduce imports, the Union government in December 2021 announced an incentive policy with an initial overlay of nearly \$10 billion. This aimed at attracting display fabs and semiconductor fabs (Silicon CMOS and other kinds) to "Fab in India" as well as measures to boost semiconductor ecosystem in the country.

The day after the names of the applicants for the first round were made public by the government, *Swarajya* had published a **summary** of the available information. In particular, for display fabs the two applicants are Vedanta and ELEST. However, information about their technology partners is not immediately known.